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NEW QUESTION: 1

Standard IV (A.3) relates to two major components and is titled _____ and Objectivity.

- A. Independence
- B. Impartiality
- C. None of these answers
- D. Justice
- E. Autonomy

Answer: A

Explanation:

Explanation/Reference:

Explanation:

Standard IV (A.3) - Independence and Objectivity, states that members shall use reasonable care and judgment to achieve and maintain independence and objectivity in making investment recommendations or taking investment action.

NEW QUESTION: 2

_____ is a type of event log used to identify a successful user login.

- A. TCP network media protocol.
- B. Event log analysis certificate that stands for certified event forensics.
- C. A standard log event format that is used for log collection.
- D. type of event log used to identify a successful user login.

Answer: B

NEW QUESTION: 3

which definition of common event format in terms of a security information and event management solution is true?

- A. TCP network media protocol.
- B. Event log analysis certificate that stands for certified event forensics.
- C. A standard log event format that is used for log collection.
- D. type of event log used to identify a successful user login.

Answer: C

NEW QUESTION: 4

What is the primary role of smartcards in a PKI?

- A. Transparent renewal of user keys
- B. Tamper resistant, mobile storage and application of private keys of the users
- C. Easy distribution of the certificates between the users
- D. Fast hardware encryption of the raw data

Answer: B

Explanation:

Reference: HARRIS, Shon, All-In-One CISSP Certification Exam Guide, 2001, McGraw-

Hill/Osborne, page 139;

SNYDER, J., What is a SMART CARD?.

Wikipedia has a nice definition at:

http://en.wikipedia.org/wiki/Tamper_resistance

Security

Tamper-resistant microprocessors are used to store and process private or sensitive information, such as private keys or electronic money credit. To prevent an attacker from retrieving or modifying the information, the chips are designed so that the information is not accessible through external means and can be accessed only by the embedded software, which should contain the appropriate security measures.

Examples of tamper-resistant chips include all secure cryptoprocessors, such as the IBM

4758 and chips used in smartcards, as well as the Clipper chip.

It has been argued that it is very difficult to make simple electronic devices secure against tampering, because numerous attacks are possible, including:

- * physical attack of various forms (microprobing, drills, files, solvents, etc.)
- * freezing the device
- * applying out-of-spec voltages or power surges
- * applying unusual clock signals
- * inducing software errors using radiation
- * measuring the precise time and power requirements of certain operations (see power analysis)

Tamper-resistant chips may be designed to zeroise their sensitive data (especially cryptographic keys) if they detect penetration of their security encapsulation or out-of-specification environmental parameters. A chip may even be rated for "cold zeroisation", the ability to zeroise itself even after its power supply has been crippled.

Nevertheless, the fact that an attacker may have the device in his possession for as long as he likes, and perhaps obtain numerous other samples for testing and practice, means that it is practically impossible to totally eliminate tampering by a sufficiently motivated opponent. Because of this, one of the most important elements in protecting a system is overall

system design. In particular, tamper-resistant systems should "fail gracefully" by ensuring that compromise of one device does not compromise the entire system. In this manner, the attacker can be practically restricted to attacks that cost less than the expected return from compromising a single device (plus, perhaps, a little more for kudos). Since the most sophisticated attacks have been estimated to cost several hundred thousand dollars to carry out, carefully designed systems may be invulnerable in practice.

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